

[04 ELECTRONICS & TELECOMM.]
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC 2007-08 **
EXAM YEAR : NOV-2019

SEAT NO.	P.R. NO.	ARE CD.	PRO VI.	SEX	NAME OF THE CANDIDATE	THEORY		SEM. 6	SEM. 5	GRAND	REMARKS
						MAX. 100	SESS 25	TOTAL 125	TOTAL MAX. 850 MIN. 340 PRACTICAL 50 OR 25	TOTAL MAX. 850 MIN. 340 ORAL 50 OR 25	
2036	201509002	(E)	N	M	DIXIT AKASH SUNIL						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	000	010	010A		000A	
					PERIPHERAL DEVICES & INTERFACING	000	003	003A			
					POWER ELECTRONICS	000	012	012A			
					ANTENA & WAVE PROPAGATION	000	003	003A			
					ELECTRONIC INSTRUMENTATION	000	006	006A			
					VLSI TECHNOLOGY & DESIGN	000	009	009A		000A	
2037	201610478	(E)	N	M	GADNKAR ANISH GOVIND						
					COMMUNICATION ENGINEERING II	041	010	051P		030+	
					PERIPHERAL DEVICES & INTERFACING	040	016	056+			
					POWER ELECTRONICS	042	018	060+			
					ANTENA & WAVE PROPAGATION	042	017	059+			
					ELECTRONIC INSTRUMENTATION	045	014	059+			
					VLSI TECHNOLOGY & DESIGN	038	\$2 015	053+	\$2	030+	
									398	\$02 P 415	\$03 C 0813 \$05 C
2038	201508935	(E)	N	M	GAUNS SHIVAM SADASHIV						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	000	003	003A		020+	
					PERIPHERAL DEVICES & INTERFACING	000	005	005A			
					POWER ELECTRONICS	000	008	008A			
					ANTENA & WAVE PROPAGATION	000	006	006A			
					ELECTRONIC INSTRUMENTATION	000	003	003A			
					VLSI TECHNOLOGY & DESIGN	000	004	004A		026+	
2039	201305697	(E)	N	M	GAWDE ASHISH EKNATH						
					COMMUNICATION ENGINEERING II	000	011	011A		028+	
					PERIPHERAL DEVICES & INTERFACING	054	016	070E			
					POWER ELECTRONICS	016	016	032F			
					ANTENA & WAVE PROPAGATION	043	010	053+			
					ELECTRONIC INSTRUMENTATION	000	010	010A			
					VLSI TECHNOLOGY & DESIGN	037	\$3 011	048+	\$3	032+	
									284	\$03 F 378	\$12 P 0662 \$15 F
2040	201509107	(E)	N	M	KENI ADITYA SHIRISH						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	052	006	058+		041+	
					PERIPHERAL DEVICES & INTERFACING	025	007	032F			
					POWER ELECTRONICS	040	011	051+			
					ANTENA & WAVE PROPAGATION	042	005	047E	\$3		
					ELECTRONIC INSTRUMENTATION	036	007	043F			
					VLSI TECHNOLOGY & DESIGN	060	003	063E		032+	

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						TOTAL	PRACTICAL	ORAL	TOTAL	ORAL		
						MAX. 850	MIN. 340	50 OR 25	MAX. 850	MIN. 340	MAX. 1700	MIN. 0680
						THEORY						
						MAX. 100			SESS 25	TOTAL 125		
2041	201509193	(E)	N	M	KUNDAIKAR SIDDHANT VASUDEV				** RESULT WITHHELD - (SEM. 5 NOT PASSED) **			
					COMMUNICATION ENGINEERING II	035	\$5	015	050+	\$5	020+	
					PERIPHERAL DEVICES & INTERFACING	040		013	053+			
					POWER ELECTRONICS	000		013	013A			
					ANTENA & WAVE PROPAGATION	052		017	069+			
					ELECTRONIC INSTRUMENTATION	041		010	051+			
					VLSI TECHNOLOGY & DESIGN	043		010	053+		024+	
2042	201305708	(E)	N	F	TARI SHREYA SHAMSUNDAR							
					COMMUNICATION ENGINEERING II	048		016	064+		020+	
					PERIPHERAL DEVICES & INTERFACING	044		005	049P	\$1		
					POWER ELECTRONICS	045		013	058+			
					ANTENA & WAVE PROPAGATION	044		017	061+			
					ELECTRONIC INSTRUMENTATION	047		010	057+			
					VLSI TECHNOLOGY & DESIGN	035	\$5	012	047+	\$5	031+	
									387	\$06 P	338	\$13 C
											0725	\$19 C
2267	201610653	(A)	N	M	SELLOYD NICK FERNANDES				** RESULT WITHHELD - (SEM. 5 NOT PASSED) **			
					COMMUNICATION ENGINEERING II	026		010	036F		022E	
					PERIPHERAL DEVICES & INTERFACING	035		006	041F			
					POWER ELECTRONICS	035	\$5	011	046E	\$5		
					ANTENA & WAVE PROPAGATION	000		008	008A			
					ELECTRONIC INSTRUMENTATION	041		004	045E	\$5		
					VLSI TECHNOLOGY & DESIGN	047		015	062E		028E	

READ BY *A* CHECKED BY *l*

13 FEB 2020

DECLARED ON

ASST. REGISTRAR-E (PROF) *[Signature]*

ASST. REGISTRAR (EXAM) *[Signature]*

CONTROLLER OF EXAMINATION *[Signature]*